

Electronic Acknowledgement Receipt

EFS ID:	4787528
Application Number:	10714935
International Application Number:	
Confirmation Number:	3704
Title of Invention:	Shift register block, and data signal line driving circuit and display device using the same
First Named Inventor/Applicant Name:	Kazuhiro Maeda
Customer Number:	23117
Filer:	MICHAEL J SHEA
Filer Authorized By:	
Attorney Docket Number:	1035-483
Receipt Date:	12-FEB-2009
Filing Date:	18-NOV-2003
Time Stamp:	22:35:32
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$880
RAM confirmation Number	4857
Deposit Account	141140
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Fee Worksheet (PTO-06)	1035-483-cvr.pdf	419472 <small>eb3c361d51ae9b3dc986eb26b3c3a81c3a70a</small>	no	1
Warnings:					
Information:					
2		1035-483-amd.pdf	4799866 <small>9f884e182b0e9e327c183116e2e41c62ed5a1204</small>	yes	17
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment/Req. Reconsideration-After Non-Final Reject		1	1	
	Claims		2	15	
	Applicant Arguments/Remarks Made in an Amendment		16	17	
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	30077 <small>7bc5941ee8f72014f816cc0f916524e170e9f6</small>	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			5249415		
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